

uPAC-7186EX-FM-X305B SPECIFICATIONS :

Core specifications	
CPU	80186 CPU (80MHz)
SRAM	512K Bytes
Flash	512K Bytes; Erase unit is one sector (64K bytes); 100,000 erase/write cycles.
NAND Flash	64M x 8 Bit
EEPROM	16K bytes Data retention > 100 years; 1,000,000 erase/write cycles
NVRAM	31 Bytes (battery backup, data valid up to 10 year)
RTC (real time clock)	Year-2000 compliance; seconds, minutes, hours, date of the month; month, year, valid up from 1980 to 2079
64-bit Hardware Serial Number	Yes
Build-in Watchdog Timer	Yes
Communication Interface	
Ethernet Port	10/100 Mbps Auto-negotiating; Auto-MDIX; LED indicator
COM1	RS-232 (TXD, RXD, CTS, RTS and GND); Program download port.
COM2	RS-485 (D2+, D2-, self-tuner ASIC inside)
COM3	RS-232 (TXD, RXD and GND) → X305B
Baud rate 115.2K bps Max	For COM1, COM2, COM3
Digital Output → X305B	
Output Channel	1
Output Type	Open-Collector (NPN)
Load Voltage	30VDC, Max
Load Current	100mA, Max
Isolation Voltage	Non-isolation
Digital Input → X305B	
Input Channel	1
Input Type	Sink, non-isolated channel with common ground
Off Voltage Level	+1V max.
On Voltage Level	+3.5V ~ +30V
Isolation Voltage	Non-isolation
Analog Input → X305B	
Input Channels	7
Resolution	12-Bit
Input Range/Type	0 ~ 20mA/Single-ended
Isolation Voltage	Non-isolation
Other specifications	
5-Digit LED Display	No
Red LED	Yes
Casing	Yes

Dimensions	123 mm x 72 mm x 33 mm
Power requirement	+10 to 30Vdc, Non-isolation
Environment	
Environment	Operating Temperature : -25 ~ +75 °C
	Storage Temperature : -30 ~ +80 °C
	Relative Humidity : 10% to 90% Non-condensing

Digital Input Wire Connection

Input Type	ON State DI value as 0	OFF State DI value as 1
Relay Contact		
TTL/CMOS Logic		
Open Collector		

Digital Output Wire Connection

Input Type	ON State DO value as 0	OFF State DO value as 1
Drive Relay		
Resistance Load		

Voltage Input Wire Connection

Input Type	
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